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Application Number	10/773,900
Filing Date	02/06/2004
First Named Inventor	Samir Chaudhry
Art Unit	2814
Examiner Name	Doan, Theresa T.
Attorney Docket Number	Chaudhry 28-20-10-14-4/075903-289

Total Number of Pages in This Submission

### ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance communication to Technology Center (TC)
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<input type="checkbox"/> After Final	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Proprietary Information
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### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	John L. DeAngelis, Jr. Reg. No. 30,622
Signature	Beusse Brownlee Wolter Moya & Maire, P.A.
Date	August 26, 2005

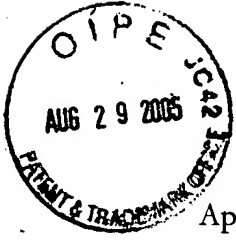
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: **Samir Chaudhry, et al**

Group Art Unit: **2814**

Serial No.: **10/773,900**

Examiner: **Doan, Theresa T.**

Filed: **02/06/2004**

Confirmation No.: **8328**

Atty. Docket No.: **Chaudhry 28-20-10-14-4/075903-289**

For: **Vertical Replacement-Gate Silicon-on-Insulator Transistor**

**RESPONSE TO ELECTION/RESTRICTION REQUIREMENT**

August 26, 2005

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Election/Restriction Requirement mailed on August 9, 2005, the Applicants hereby amend the above-identified patent application as follows.